

SC11448TP P01

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First Named Inventor

Rana P. Singh

Group Art Unit

Examiner Name

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Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

 τ^2

K.V. Rao et al., "Trench Capacitor Design Issues In VLSI DRAM Cells", 1986 IEEE, Section 6, 4 pgs.

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